

AMENDED SPECIFICATION

On page 8 of the specification, the paragraph starting at line 7 is replaced with the following paragraph:

The binary current source (17) receives the alternating current output signal from the variable duty cycle oscillator (2) by way of the AC signal input port (20), a first sample from the negative voltage source input port (12) by way of the ~~and a~~ negative supply input port (46), and the error signal from the output comparator circuit (7) by way of the error signal input port (21) and outputs a binary switching signal, whereby the binary switching signal is a function of the alternating current output signal and the error signal.